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APPLICANT: SONY CORP;

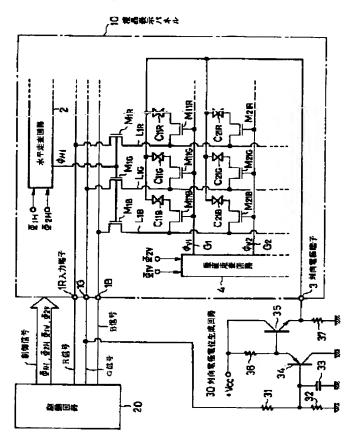
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TITLE

LIQUID CRYSTAL DISPLAY DEVICE



ABSTRACT:

PURPOSE: To control it so that a DC component of a voltage applied to a liquid crystal becomes zero always.

CONSTITUTION: An R signal, a G signal, a B signal from a drive circuit 20 are supplied to input terminals 1R, 1G, 1B of a liquid crystal display device 10 respectively. Further, control signals such as clock signals  $\Phi_1H$ ,  $\Phi_2H$ ,  $\Phi_1V$ , etc., from the drive circuit 20 are supplied to a liquid crystal display panel 10. Further, the G signal from the drive circuit 20. is supplied to a counter electrode potential generation circuit 30. In the counter electrode potential generation circuit 30, the G signal from the drive circuit 20 is supplied to a capacitor 33 constituting an integration circuit through divider resistors 31, 32. Then, potential integrated by the capacitor 33 is supplied to the counter electrode terminal 3 of the liquid crystal display panel 10 through transistors 34, 35 constituting an emitter follower circuit. Further, the circuit 30 is provided with bias resistors 36, 37.

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